dam 13

PAT-NO:

JP411219299A

DOCUMENT-IDENTIFIER: JP 11219299 A

TITLE:

MICROCOMPUTER

PUBN-DATE:

August 10, 1999

INVENTOR - INFORMATION:

COUNTRY

FUDEYASU, NORIHIKO

N/A

ABE, SHINSUKE

N/A

ASSIGNEE - INFORMATION:

NAME

COUNTRY

MITSUBISHI ELECTRIC CORP

N/A

APPL-NO:

JP10021305

APPL-DATE:

February 2, 1998

INT-CL (IPC): G06F009/445, G06F009/46

ABSTRACT:

PROBLEM TO BE SOLVED: To perform a free interruption processing from an E/W program for erasing/writing a flash ROM transferred to a RAM by using a boot program.

SOLUTION: In a boot mode for transferring the boot program for transferring the E/W program for rewriting the data of the flash ROM 3 from the outside to the RAM, from a boot ROM 4 to the RAM 2 and reading and executing the boot program transferred to the RAM 2 from a CPU 1, by arranging a reset interruption vector inside the boot ROM 4 and all the other interruption

vectors inside the RAM 2 respectively, the execution of the interruption processing from the E/W program transferred to the RAM 2 is made possible.

COPYRIGHT: (C)1999, JPO